

HOW TO APPLY/REGISTRATION/COURSE FEE

Date Type	Date
Registration Start Date	27.05.2024
Last Date of Registration	09.06.2024
Last Date for Deposition of Registration Fee	09.06.2024
Commencement of Training Programme	10.06.2024

A fee shall be paid by online mode. The registration fee will include registration kit, high tea. The selection is on a first come first served basis depending upon the availability of seats. Registration charges are non refundable for selected participants. Online registration can be done in following three steps :

Step 1: Register by going through the following link or by scanning QR code given on next page.

[Google Form Link](#)

Step 2: Fill the google form and upload the required documents.

Step 3: Submit the google form.

Note: Separate charge as per university norms will be applicable if any student is allowed for hostel accommodation during internship period.

Eligibility:

Students of various institutions comprising of B.Tech / M.Tech to register.

Account Details.

Account No.: 33542824744

IFSC Code : SBIN0002578

Account Name: Madan Mohan Malaviya University of Technology.

Bank Name: State Bank of India

FEE DETAILS

Fee Type	Registration Fee
Student Registration	INR 6,000/-

DESCRIPTION

First four week will be utilized for training on VLSI design tools and last two week will be dedicated for project work.

SPEAKERS / INSTRUCTORS

- Prof. R. K. Chauhan, ECED, MMMUT, Gorakhpur
- Dr. B. P. Pandey, ECED, MMMUT, Gorakhpur
- Dr. Narendra Yadava, DDU, Gorakhpur
- Dr. Mangaldeep Gupta, BBAU, Lucknow
- Mr. Saurabh Kumar, ECED, MMMUT, Gorakhpur
- Mr. Manish Deo, ECED, MMMUT, Gorakhpur
- Mr. Prince Kumar Singh, , MMMUT, Gorakhpur
- Mrs. Jyotsana Singh, ECED, MMMUT, Gorakhpur
- Mrs. Pooja Chaudhary, ECED, MMMUT, Gorakhpur
- Mrs. Shweta Yadav, ECED, MMMUT, Gorakhpur

REGISTER HERE

SCAN HERE



CONTACT US

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SIX-WEEK SUMMER INTERNSHIP ON TCAD / EDA TOOLS FOR VLSI APPLICATIONS - 2024

10 June to 19 July 2024

Organised by



VLSI RESEARCH LAB, DEPARTMENT OF
ELECTRONICS AND COMMUNICATION
ENGINEERING
(NBA ACCREDITED)

MADAN MOHAN MALAVIYA UNIVERSITY OF
TECHNOLOGY, GORAKHPUR (UP)

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Vice Chancellor
MMMUT, Gorakhpur

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HOD, Electronics and Communication
Engineering, MMMUT, Gorakhpur

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Prof. R. K. Chauhan
Electronics and Communication Engineering,
MMMUT, Gorakhpur

Co-Coordinator

Dr. B. P. Pandey
Electronics and Communication Engineering,
MMMUT, Gorakhpur

Date	Topics to be Covered	Remarks
<p>Week 1: 10th June to 14th June 2024</p>	<p>Training on TCAD Tools (Synopsys & Silvaco) for design and simulations of Semiconductor Devices</p>	
<p>Week 2: 18th June to 22nd June 2024</p>	<p>Training on Cadence Tool for design and simulations of low power circuit.</p>	
<p>Week 3: 24th June to 28th June 2024</p>	<p>Training on Atlas TCAD tool for design and simulations of MOS Devices</p>	
<p>Week 4: 1st July to 5th July 2024</p>	<p>Training on Sentaurus TCAD tool for design and simulations of MOS Devices Training on SCAP 1D Tool for design and simulations of photovoltaic devices. Simulations on FPGA Kit</p>	<p>Weekly schedule may be interchanged according to the availability of the experts</p>
<p>Week 5: 8th July to 12th July 2024</p>	<p>Projects on the TCAD tool to be undertaken by participating students. Guidance and assistance provided for project development and research paper writing.</p>	
<p>Week 6: 15th July to 19th July 2024</p>	<p>Final project presentations by students followed by evaluations and feedback.</p>	