



Madan Mohan Malaviya University of Technology Gorakhpur-273010 (U.P.) India

(Department of Electronics & Communication Engineering)

Advertisement for the position of PA/TA

Applications are invited from highly motivated candidates for the position of Project Associate (Asst.PA/PA/Sr. PA) and Technical Assistant (TA) to work in the C2S, a MeitY sponsored project entitled “**Design of Low Power Memory Circuits**” under the supervision of undersigned. The position is temporary and is available immediately. The selected candidate for Asst.PA and PA may be permitted to register for Ph.D. program as per the existing university rules.

Positions: Two Project Associate (Asst.PA/PA/Sr. PA) and one Technical Assistant (TA) position

Duration: 5.0 years or till the completion of the project which ever will be earliest (the position is purely temporary, and on the basis of performance candidates will be allowed to continue)

Position	No of Post	Essential Qualification	Monthly Emoluments
Asst. Project Associate /	2	(a) Master's degree in Electronics engineering with first division. (b) Knowledge of any of the VLSI Design tools such as Cadence/Synopsys/Atlas is desirable.	31,000 (Consolidated)
Project Associate /		(a) Master's degree in Electronics engineering with first division. (b) 2-yrs research experience (Academic/Industry) in the field of Devices/Analog or Digital circuits with two publications in SCI Journals. (c) Knowledge of any of the VLSI Design tools such as Cadence/Synopsys/Atlas is desirable.	37,000 (Consolidated)
Sr. Project Associate		Ph.D degree in Electronics Engineering with expertise in Devices/Analog or Digital circuits simulation and having four publications in SCI Journals.	43,000 (Consolidated)
Technical Assistant	1	BCA/BSc. or Diploma in Engineering with First Division	20,000 + 6000/- (HRA)

General Information

1. Only Short-listed candidates will be communicated to appear in the Test/interview by email and no other communication in this regard will be entertained.
2. No TA/DA will be given to candidates for attending the interview.

Last Date for receipt of application: 16th October 2023 by 5:00 pm

How to Apply?

Completely filled application form should be sent to the undersigned by email. Please write “Application form for PA/TA position” in the subject. The applications will be scrutinized, and only shortlisted candidates will be intimated through email for test/interview. The Test/interview will be held **in Offline mode**, tentatively in the third week of October 2023.

Prof. R. K. Chauhan

Chief Investigator of Project

Dept of Electronics and Communication Engineering,

Madan Mohan Malaviya university of Technology-273010, U.P. India

Email id : **rkcece@mmmut.ac.in**



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APPLICATION FORM

Personal Details

Full Name (In capital)			
Date of Birth (DD/MM/YY)		Category GEN (SC/ST/OBC)	Affix a recent passport size photo
Marital Status (Single/Married)		Gender (Male/Female)	
Nationality			
Address for Communication		Permanent Address	
Mobile/Phone No.		Email:	

Details of School/University/Institute Studied (From Matriculation Onward)

S. No.	Degree	Discipline	University/Institute	Regular/ Part-time	Year	%Marks /CGPA	Division

Professional Experiences (Teaching/Research/Industrial)

Name of Organization	Designation	Nature of Work	From	To

Research Publications (if any):

List the EDA/TCAD tools you have used:

Awards, patents, prizes etc. (if any):

Any other Relevant Information:

Attachments:



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Undertaking

I understand that it is a contractual engagement on purely temporary basis for the Project. I declare that the provided information is correct to the best of my knowledge and belief. I understand that action can be taken against me if any information provided are found to be incorrect. I agree to all the terms and conditions of the associated advertisement.

Name and Signature of Applicant

Date and place: